Fig.1A

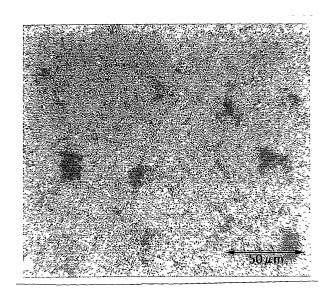


Fig.1B

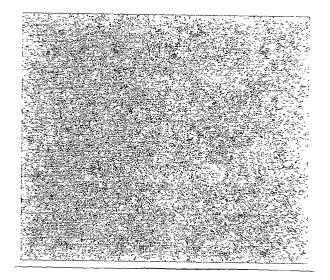


Fig.1C

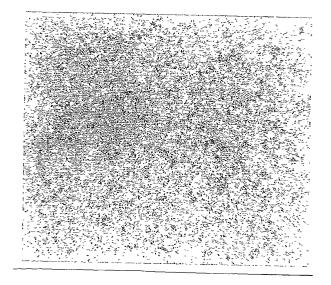


Fig.2A

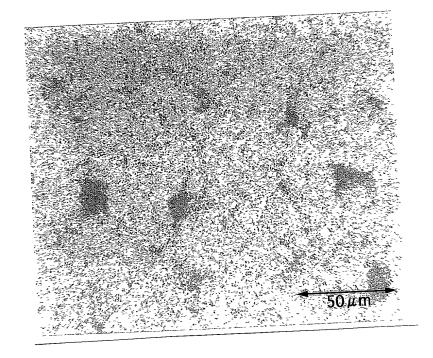


Fig.2B

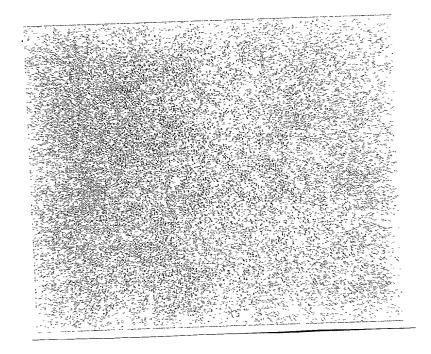


Fig.3A

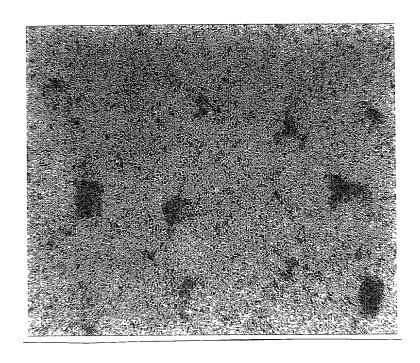
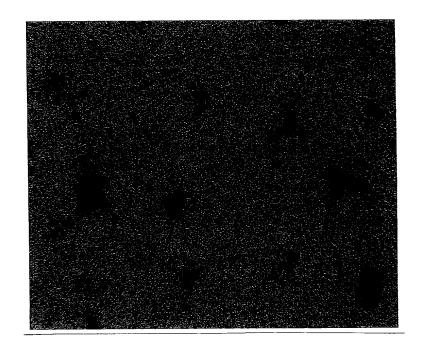


Fig.3B





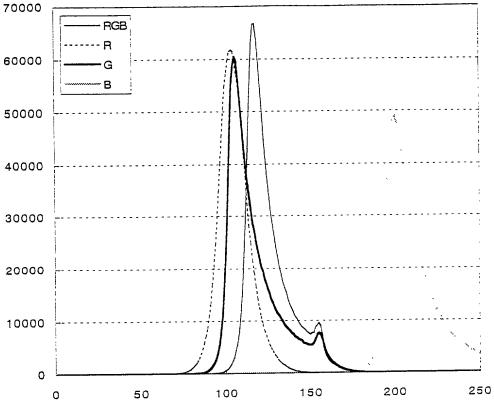


Fig.5A

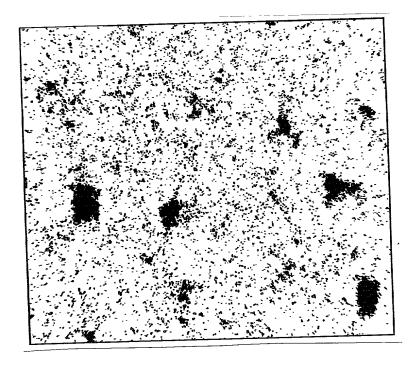
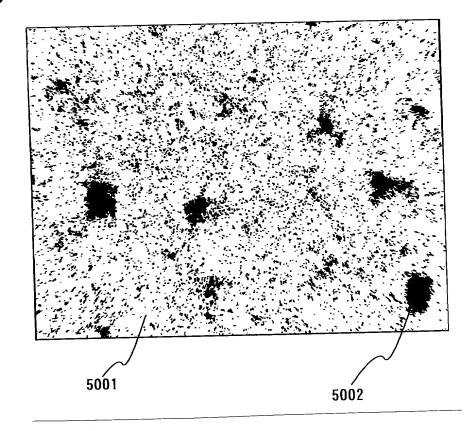
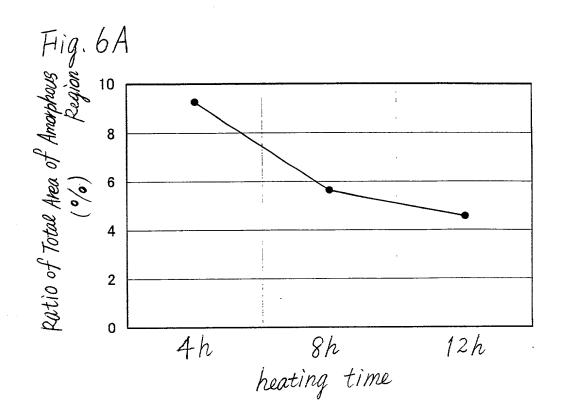
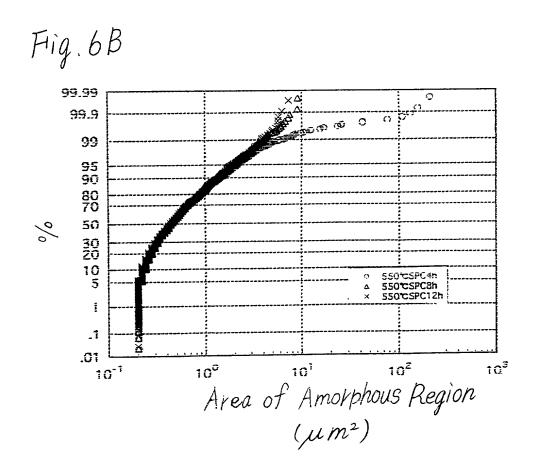
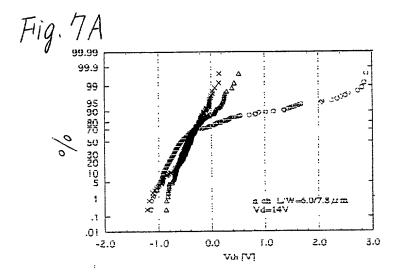


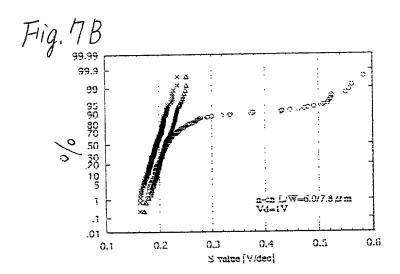
Fig.5B











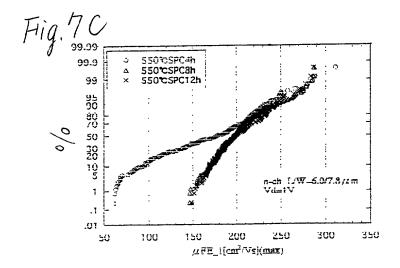


Fig.8A

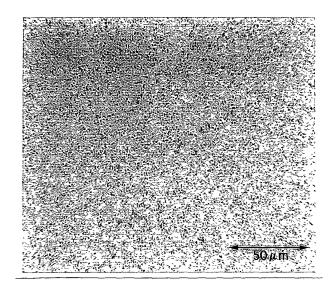


Fig.8B

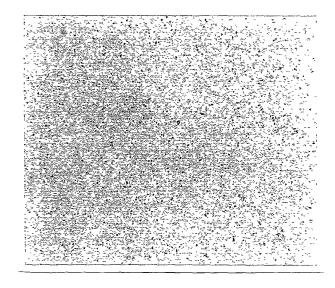
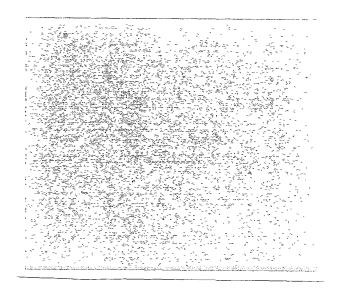
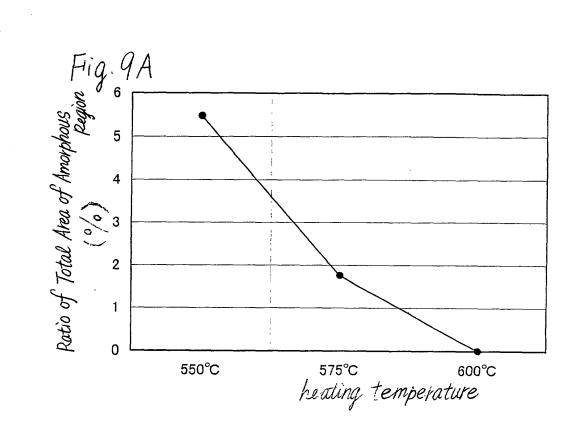
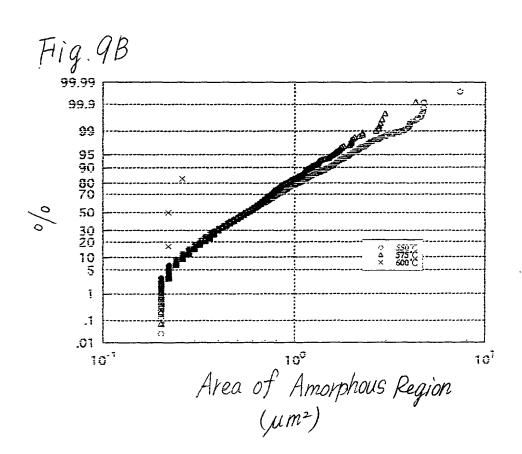
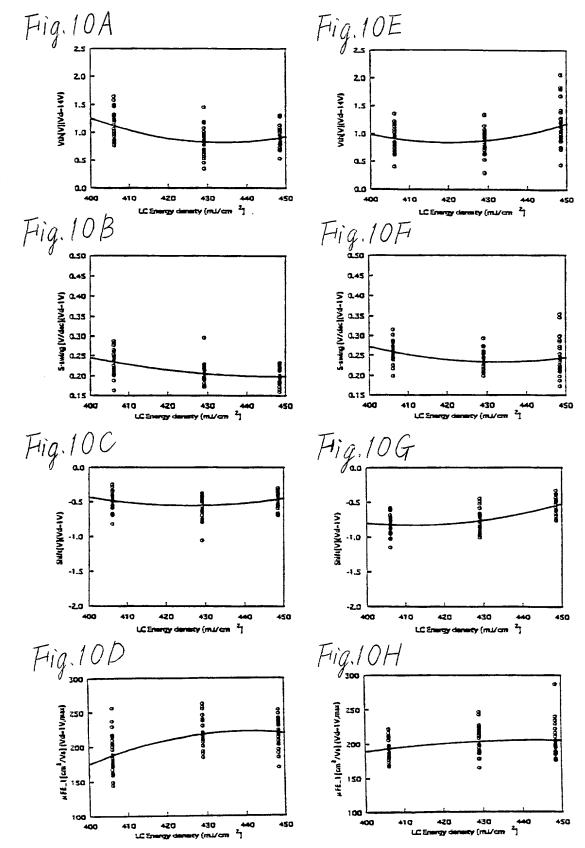


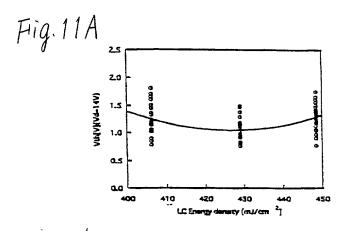
Fig.8C

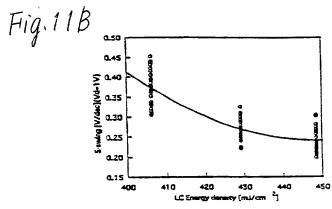


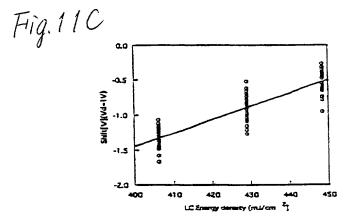


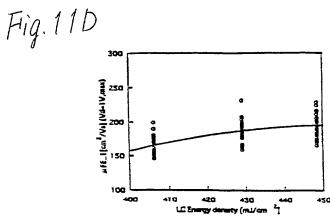












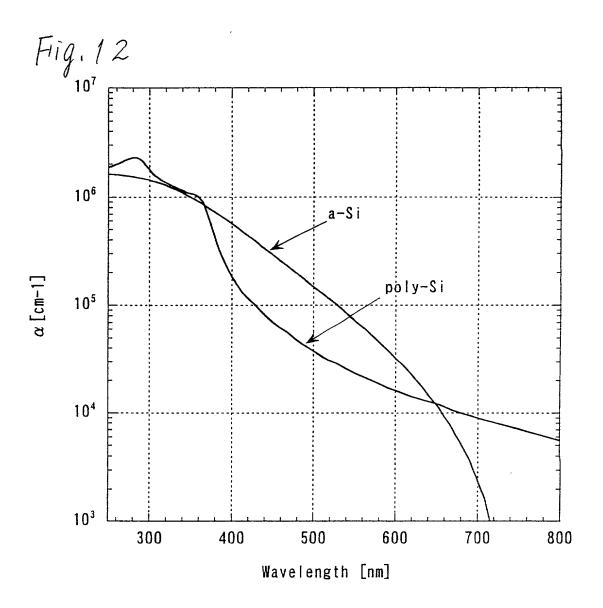
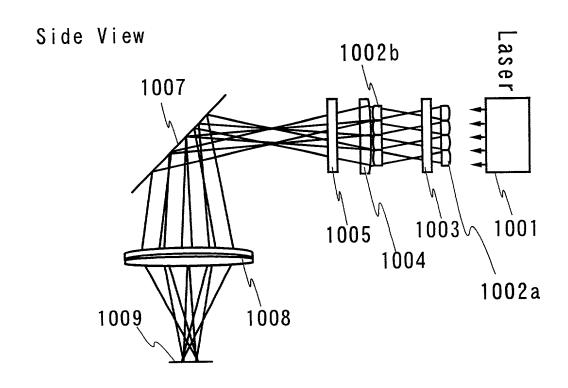
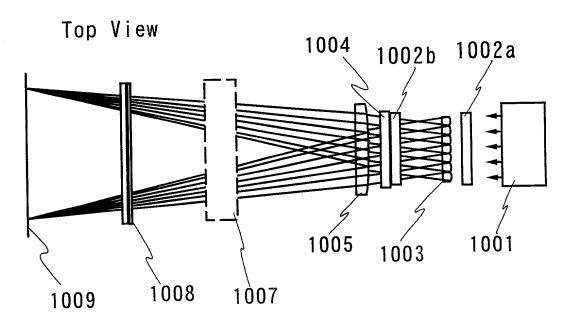
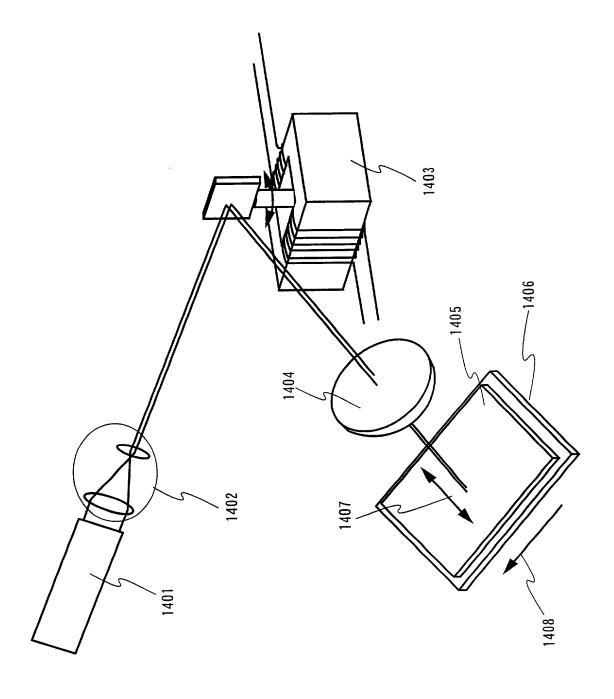
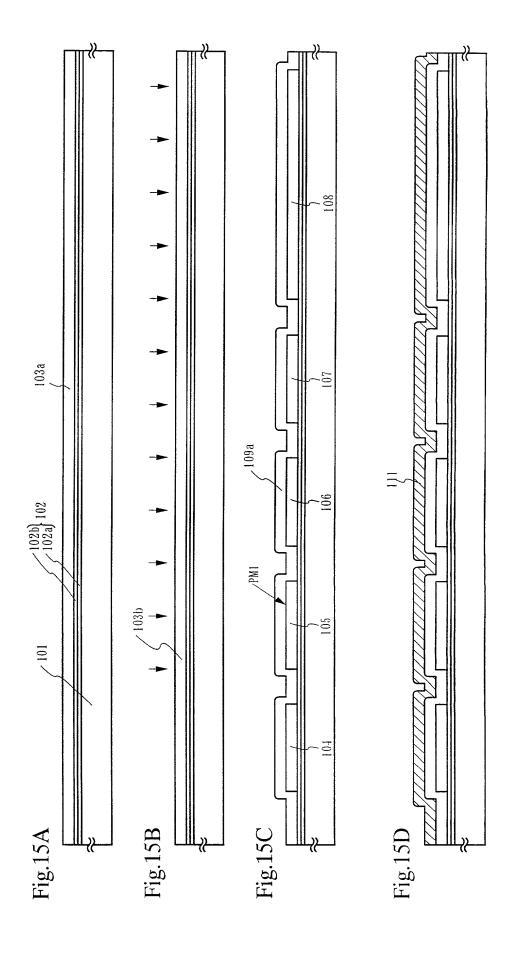


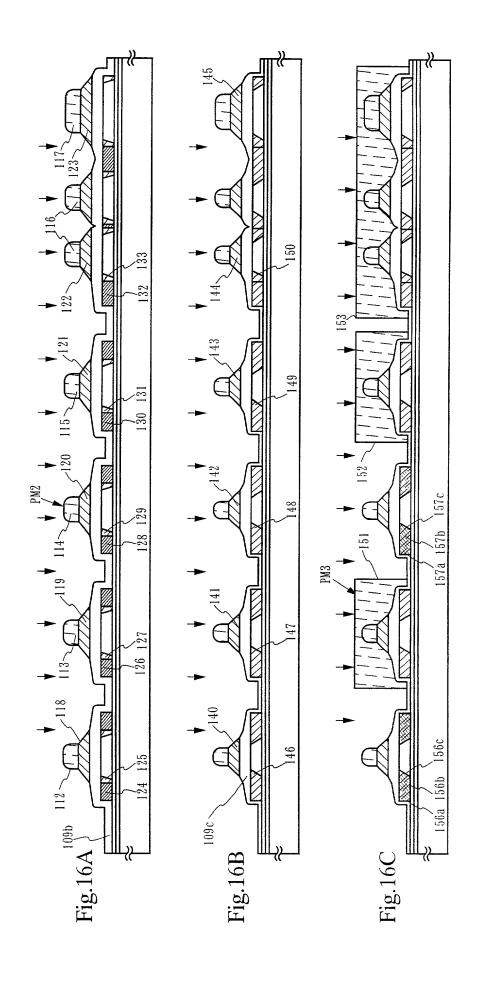
Fig.13

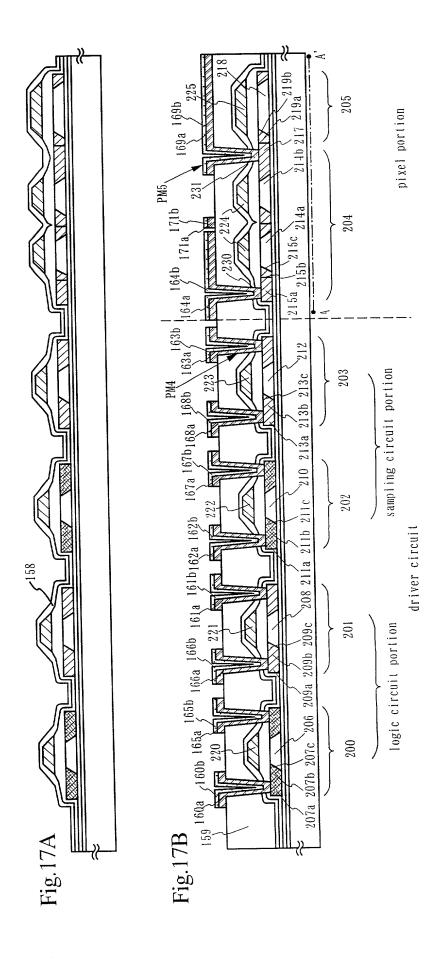


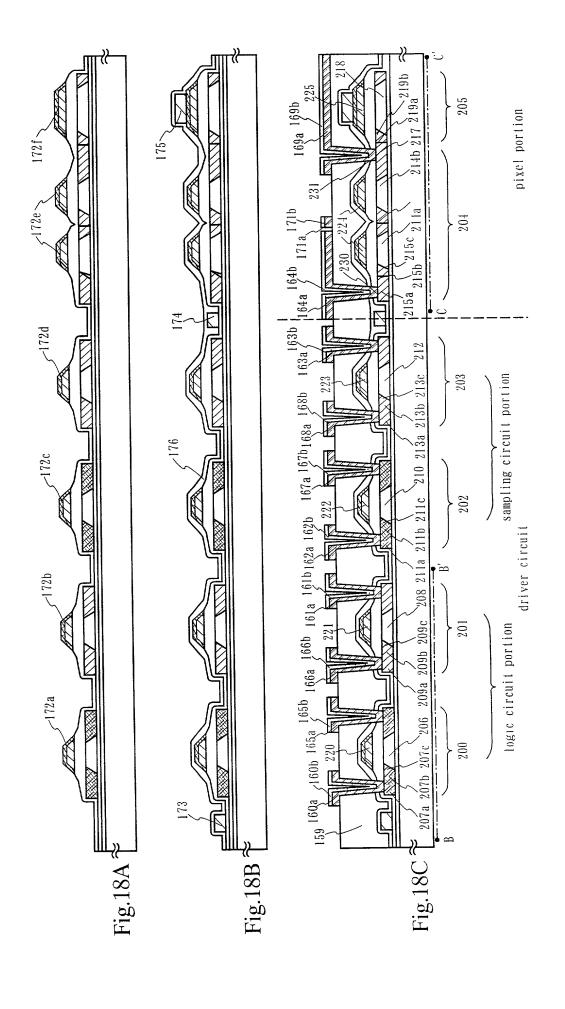












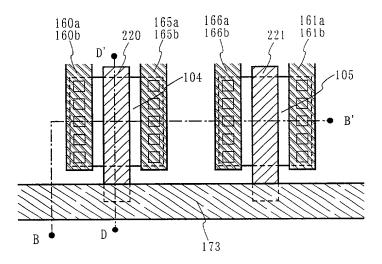


Fig.19A

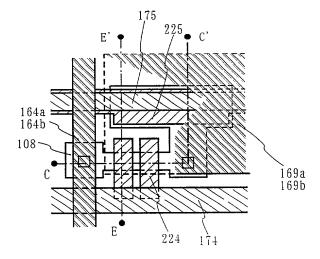
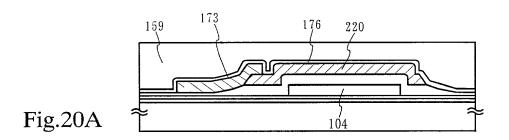
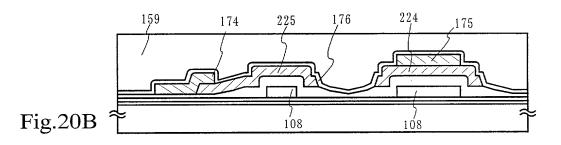
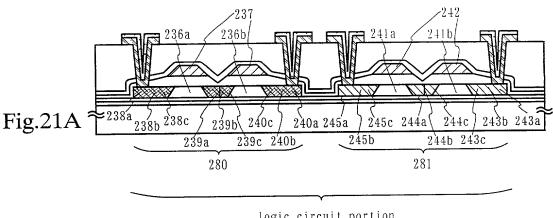
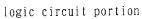


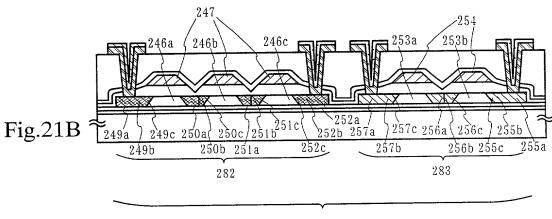
Fig.19B











sampling circuit portion

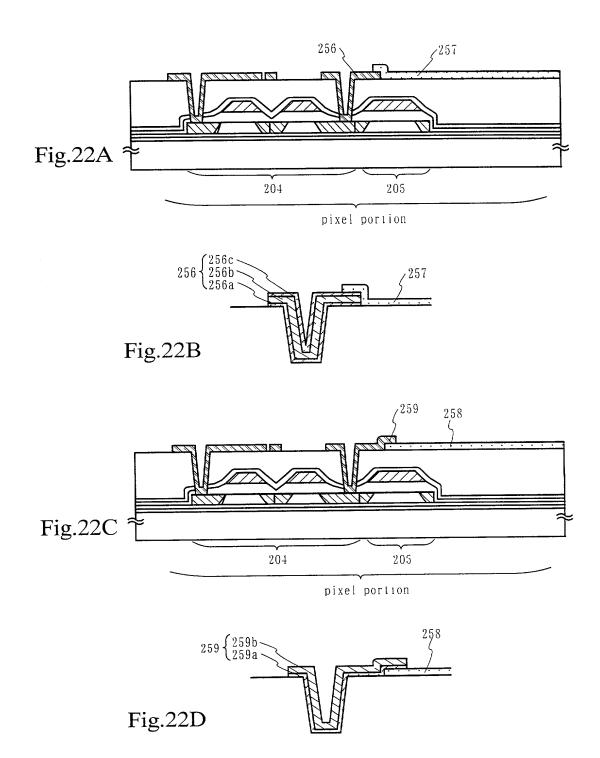
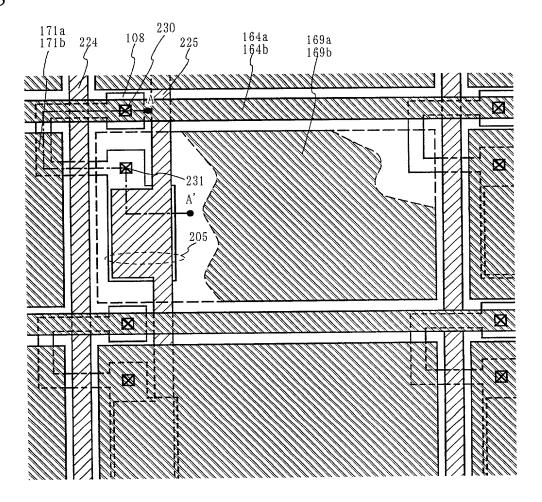
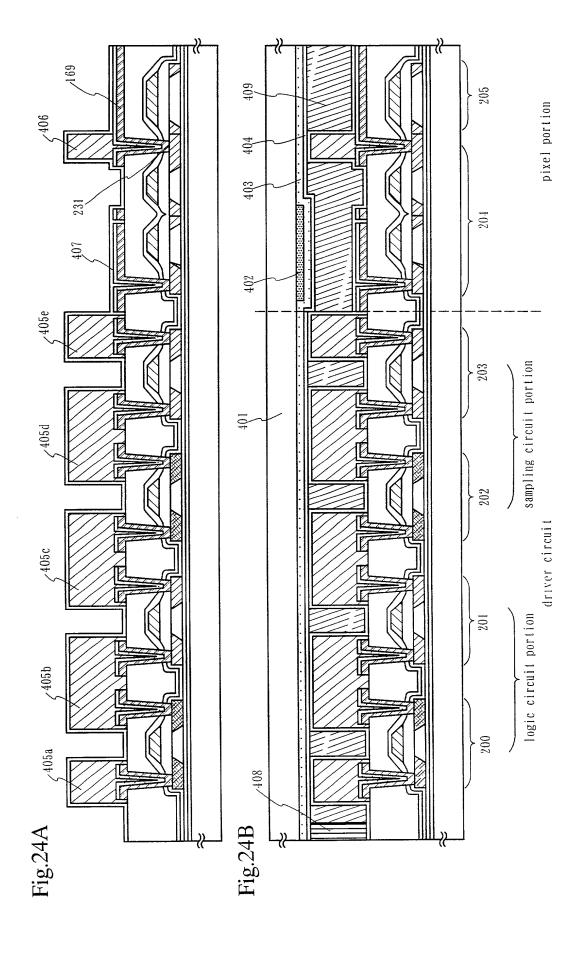


Fig.23





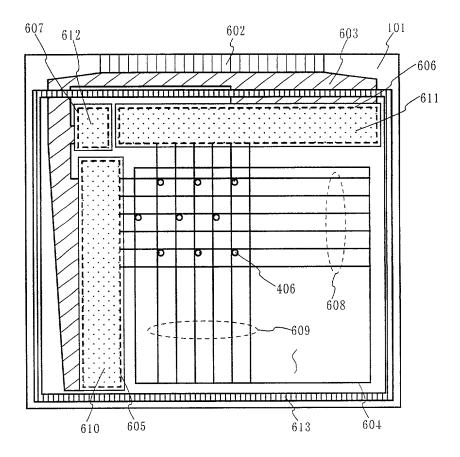


Fig.25

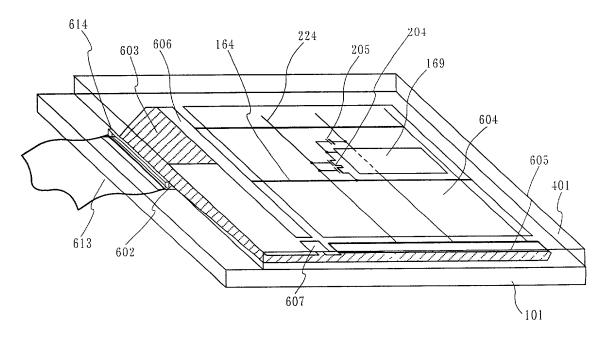


Fig.26

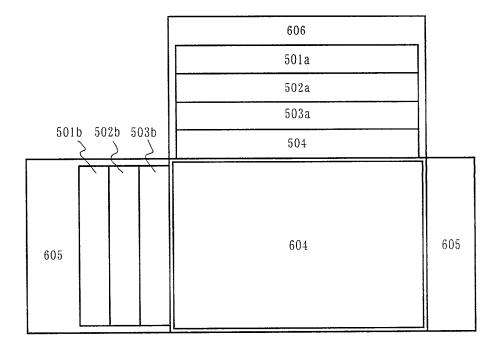
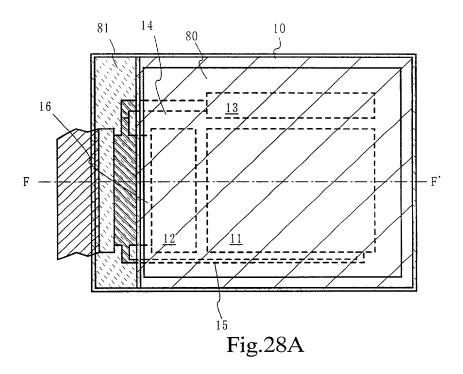
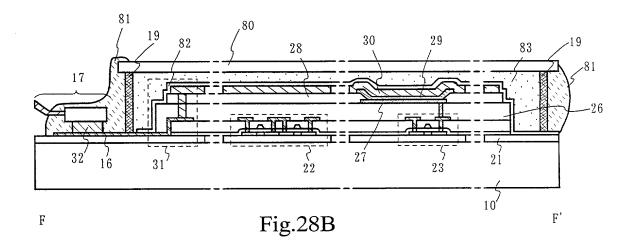
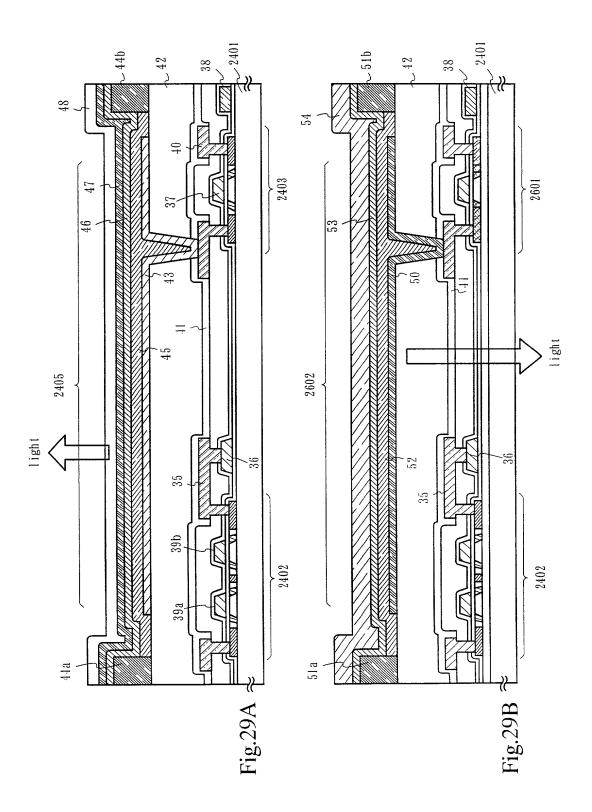


Fig.27







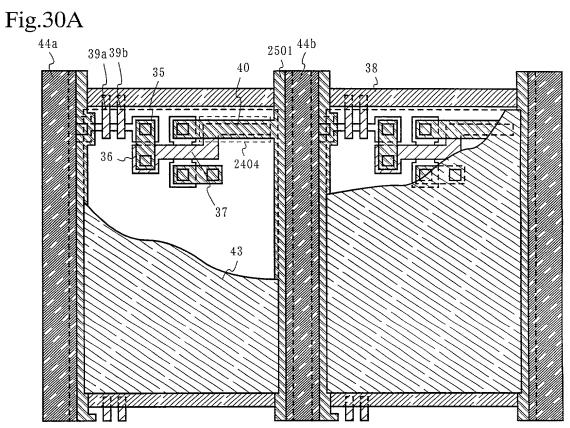


Fig.30B 

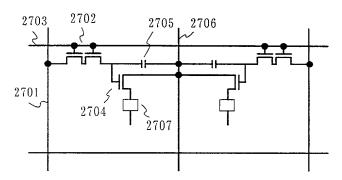


Fig.31A

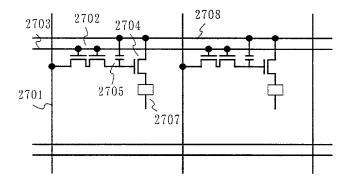


Fig.31B

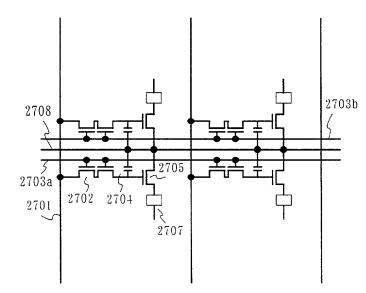
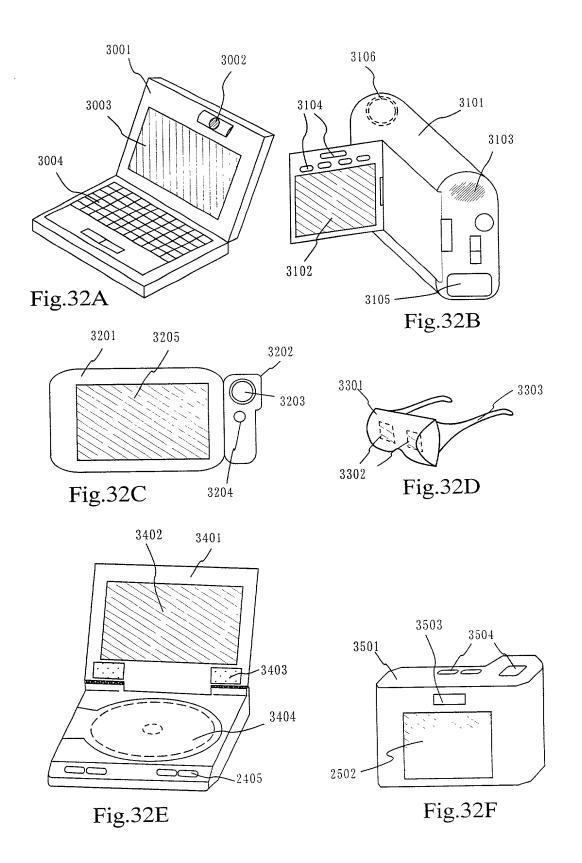
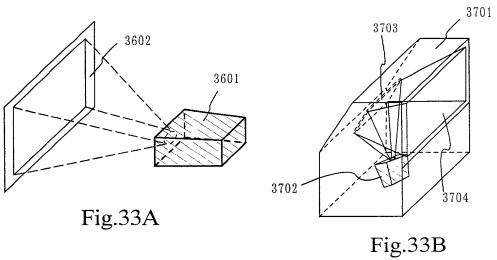
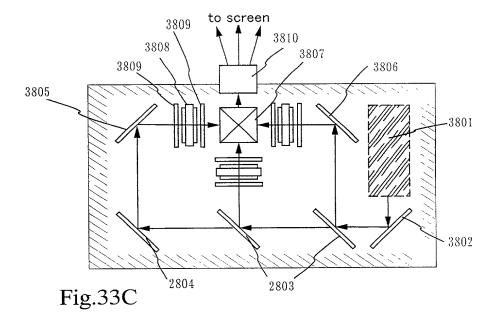


Fig.31C

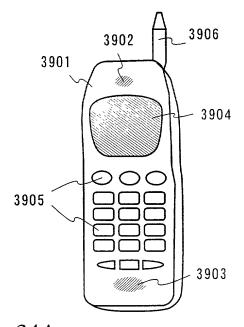


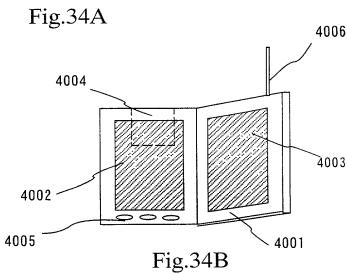




-3811 3812 3814 3813 3815 3816

Fig.33D





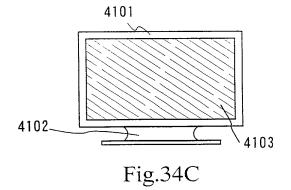


Fig.35A

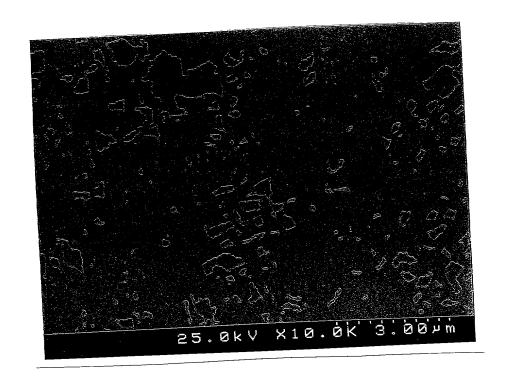


Fig.35B

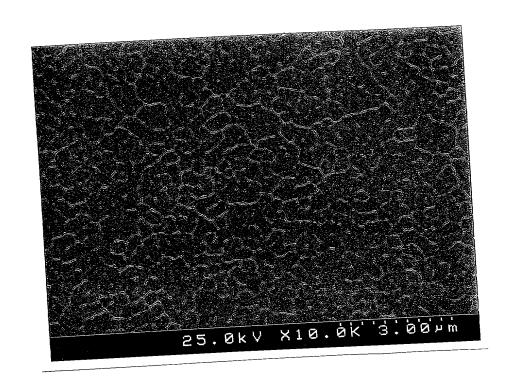


Fig.36A

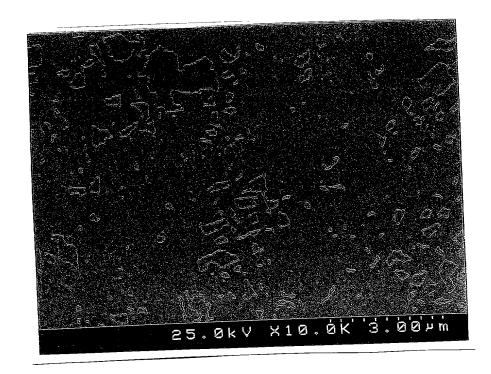


Fig.36B

